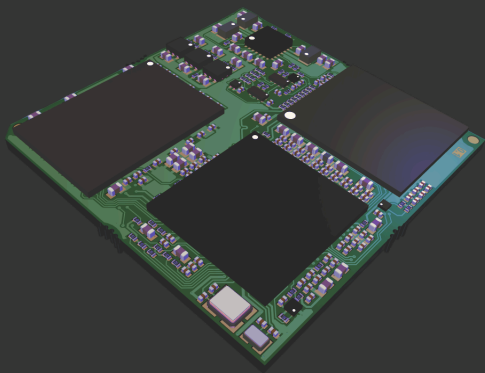


bytes**at**work

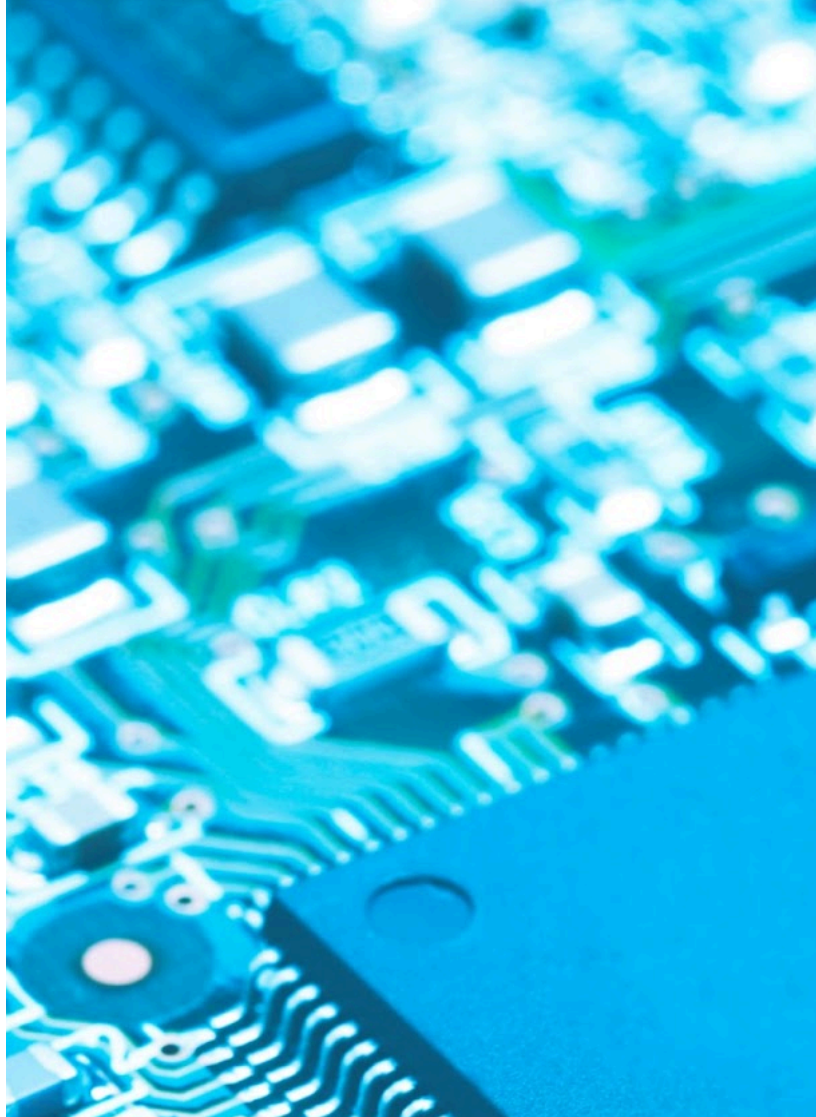
# DataSheet

---



industrial computing module

byteENGINE AM62L



## Copyright Notice

Copyright 2026 bytes at work AG, CH-8406 Winterthur, Switzerland. All rights reserved.

**Reproduction** in any form or by any means without permission is prohibited. Bytes at work AG reserves the right to make improvements to the products described in this manual at any time without notice.

No part of this manual may be reproduced, copied, translated, or transmitted in any form or by any means without the prior written permission of the company. Information provided in this manual is intended to be accurate and reliable. However, bytes at work assumes no responsibility for its use, or for any infringements upon the rights of third parties that may result from its use.

The material in this document is for product information only and is subject to change without notice. While reasonable efforts have been made in the preparation of this document to assure its accuracy, bytes at work assumes no liabilities resulting from errors or omissions in this document, or from the use of the information contained herein. bytes at work reserves the right to make changes in the product design without notice to its users.

## Trademarks

Product names, logos, brands, and other trademarks featured, or referred to are the property of their respective trademark holders. These include, but are not limited to U-Boot, Linux and ARM.

## Disclaimer

**Bytesatwork** makes no representations or warranties regarding the accuracy, completeness, timeliness, or quality of the information contained in this manual.

Bytesatwork assumes no liability for any material or non-material damages arising from the use or non-use of the information provided, or from the use of incorrect or incomplete information, unless such damages are caused by intent or gross negligence on the part of Bytesatwork.

**Bytesatwork** explicitly reserves the rights to change or add to the contents of this manual or parts of it without special notification.

## Imprint

Bytesatwork  
Technoparkstrasse 7  
CH-8406 Winterthur  
+41 52 213 79 79

info@bytesatwork.ch  
www.bytesatwork.io

## Symbols and Typographic Conventions

These symbols represent important details or aspects for working with bytesatwork-products.



### NOTICE

Follow instructions. Acting against the procedure described can lead to malfunction.



[www.bytesatwork](http://www.bytesatwork)

## Table of Contents

|                                         |           |
|-----------------------------------------|-----------|
| <b>1. Revision History</b>              | <b>4</b>  |
| <b>2. Overview</b>                      | <b>4</b>  |
| 2.1. General Information                | 4         |
| 2.2. Technical Data                     | 5         |
| 2.3. Block Diagram of AM62L             | 7         |
| 2.4. Decision guidance byteENGINE AM62L | 8         |
| 2.5. Dimensions of byteENGINE AM62L     | 9         |
| 2.6. byteENGINE AM62L OSM-S interface   | 10        |
| 2.7. TI SYSCONFIG                       | 11        |
| <b>3. Pinout functions</b>              | <b>12</b> |
| 3.1. Restrictions                       | 12        |
| 3.2. PIN List                           | 13        |
| 3.3. Power Supply                       | 24        |
| 3.4. Boot modes byteENGINE AM62L        | 24        |
| <b>4. Ordering info</b>                 | <b>26</b> |
| <b>5. References byteENGINE AM62L</b>   | <b>27</b> |

# 1. Revision History

| Hardware Revision | Marking on PCB | Release Date | Info       |
|-------------------|----------------|--------------|------------|
| 0.1               | 0.1            | March 2026   | Prototypes |



NOTICE

This document refers to  
Hardware Rev. 0.1

# 2. Overview

## 2.1. General Information

The **byteENGINE AM62L** is an industrial oriented computing module for low cost and performance optimized applications with extensive security options. It enables a short time-to-market, reducing development costs and substantial design risks.

The System on Module (SOM) uses the AM62L devices which are based on the high-performance dual ARM® Cortex®-A53 64-bit core operating at up to 1.25GHz.

The AM62L devices are built for embedded Linux applications with extensive security options and optimized power architecture. The AM62L also includes an extensive set of peripherals including modern display interfaces, making the AM62L a well-suited general purpose device for a broad range of industrial applications.

## 2.2. Technical Data

### Feature

### Details

|                 |                          |                                                                                                                                                                                                 |
|-----------------|--------------------------|-------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------|
| CPU             | Architecture             | Up to dual 64-bit ARM® Cortex®-A53                                                                                                                                                              |
|                 | CPU                      | AM62L<br>See TI Homepage for further information: <a href="#">TI AM62L</a>                                                                                                                      |
|                 | Cache                    | <ul style="list-style-type: none"> <li>&gt; Each core has a separate Cache.</li> <li>&gt; 32 kB L1 Instruction Cache</li> <li>&gt; 32 kB L1 Data Cache</li> <li>&gt; 256 kB L2 Cache</li> </ul> |
|                 | Frequency (max)          | 1.25 GHz                                                                                                                                                                                        |
|                 | Floating Point           | Floating Point Unit (FPU) with support of the ARM® single-precision data-processing instructions, and data types.                                                                               |
|                 | Security                 | Secure boot, TrustZone® based on TEE, dedicated security controller and DMA & IPC subsystem, cryptographic acceleration support                                                                 |
| Memory External | DRAM                     | Up to 2GB LPDDR4                                                                                                                                                                                |
|                 | Flash                    | Up to 128GB eMMC                                                                                                                                                                                |
| Ethernet        | Speed                    | 2x 10/100/1000 Mbps & IEEE1588                                                                                                                                                                  |
| Serial          | SPI                      | 4                                                                                                                                                                                               |
|                 | I2C                      | 5                                                                                                                                                                                               |
|                 | UART                     | 8                                                                                                                                                                                               |
|                 | McAPSSs                  | 3                                                                                                                                                                                               |
|                 | CAN-FD                   | 3                                                                                                                                                                                               |
| USB             | USB 2.0                  | 2                                                                                                                                                                                               |
| Multimedia      | MMC/SD Interface         | <ul style="list-style-type: none"> <li>&gt; 1x 8-bit eMMC Interface</li> <li>&gt; 2x 4-bit SD/SDIO interface</li> <li>&gt; Compliant with eMMC 5.1</li> </ul>                                   |
|                 | MIPI DSI                 | > 4-lanes DPHY                                                                                                                                                                                  |
| ADC             | Analog Digital Converter | 1x 12-bit ADC                                                                                                                                                                                   |
| Miscellaneous   | Watchdog                 | Yes                                                                                                                                                                                             |
|                 | GPIO                     | Up to 24                                                                                                                                                                                        |
|                 | PWM                      | 3x ePWM                                                                                                                                                                                         |

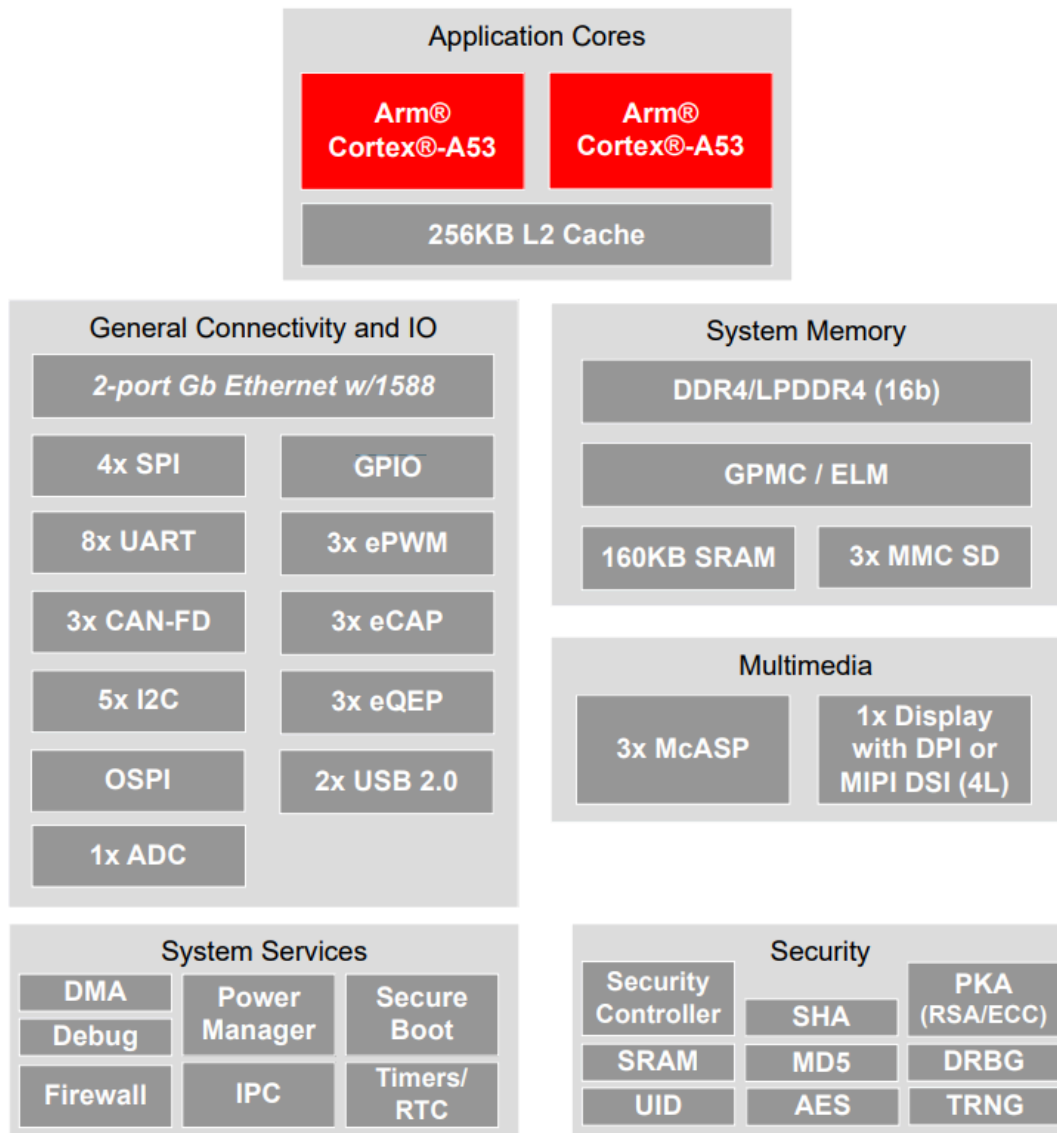
|                               |                              |                                                                                                               |
|-------------------------------|------------------------------|---------------------------------------------------------------------------------------------------------------|
|                               | <b>Boot Mode</b>             | Secure Boot                                                                                                   |
| <b>Mechanical Information</b> | <b>Input Voltage</b>         | 5,0V                                                                                                          |
|                               | <b>Power Consumption</b>     | 2 W                                                                                                           |
|                               | <b>Dimensions</b>            | 30 x 30 mm                                                                                                    |
|                               | <b>Operating Temperature</b> | Industrial temperature range: -40 to +85° C<br>Consumer temperature range: 0 to 85° C                         |
|                               | <b>Connector</b>             | OSM-S                                                                                                         |
| <b>Operating System</b>       | <b>Linux</b>                 | <a href="https://github.com/bytesatwork/meta-bytesatwork">https://github.com/bytesatwork/meta-bytesatwork</a> |

## 2.3. Block Diagram of AM62L

The **AM62L** family is designed low-cost and performance optimized. The combination of extensive security options with a wide range of peripherals make it a well-suited general-purpose device for a broad range of industrial applications. The ARM® Cortex® A-53 processors run with speed up to 1.25 GHz.

### Key features of AM62L

- > Single or Dual ARM® Cortex®-A53 up to 1.25 GHz
- > Cost-effective Architecture
- > Extensive security options and wide range of peripherals





For further information regarding the AM62L CPU, please visit the homepage of TI:  
[AM62L-Series](#)

## 2.4. Decision guidance byteENGINE AM62L

The following four steps help identifying the suitable byteENGINE for the specific customer application.

> **Step 1:** Select the needed CPU for the SOM

**>> Choose Dual or Single Arm® Cortex®-A53**

Dual Core:           AM62L32

Single Core:        AM62L31

> **Step 2:** Select the needed flash memory capacity

**> Choose eMMC 8 / 16 / 32 / 64 / 128 GB**

> **Step 3:** Select the needed RAM capacity

**> Choose 512 / 1024 / 1536 / 2048 MB**

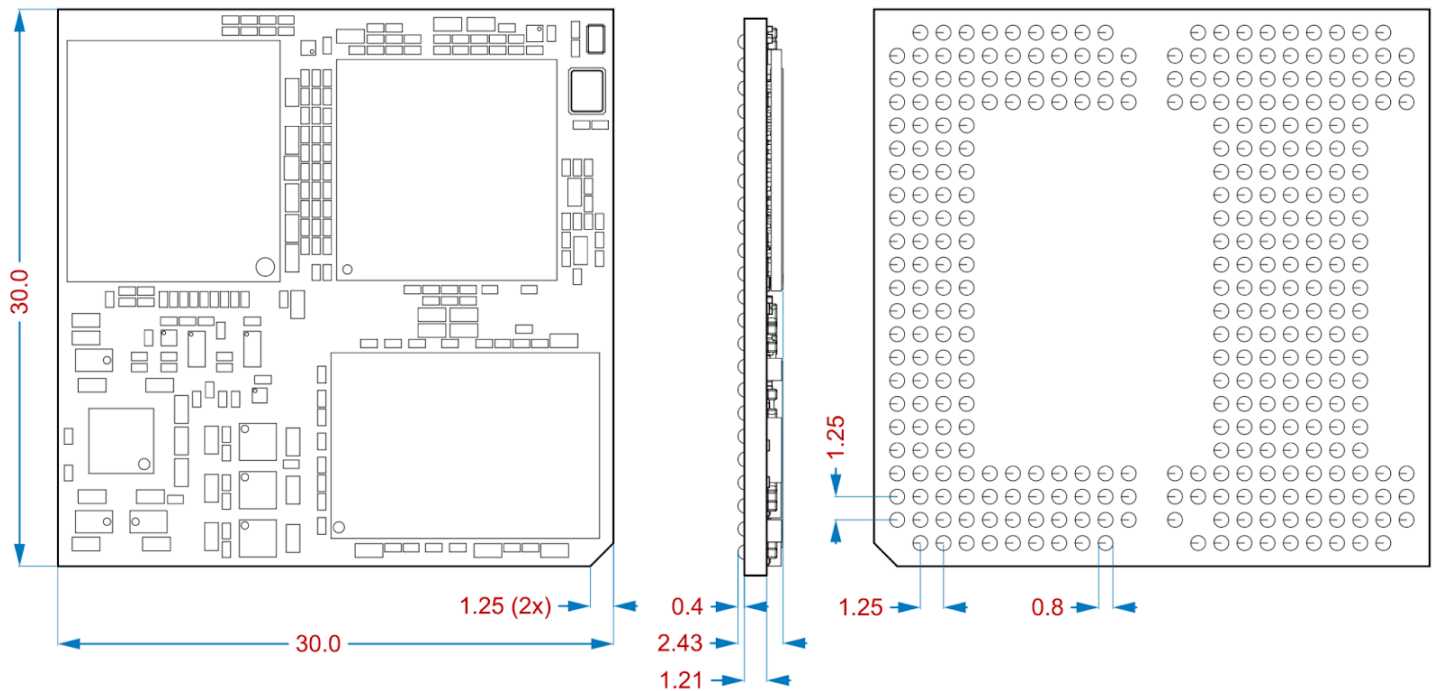
> **Step 4:** Select the needed temperature range

**> Choose consumer or industrial**

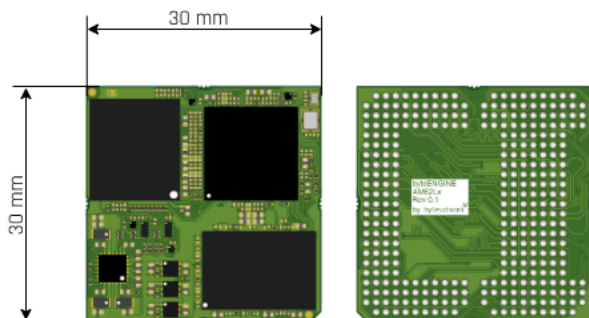
## 2.5. Dimensions of byteENGINE AM62L

The following illustration shows all important dimensions for mounting and installation of the industrial computing module byteENGINE AM62L.

> All dimensions are indicated in millimetres (mm).

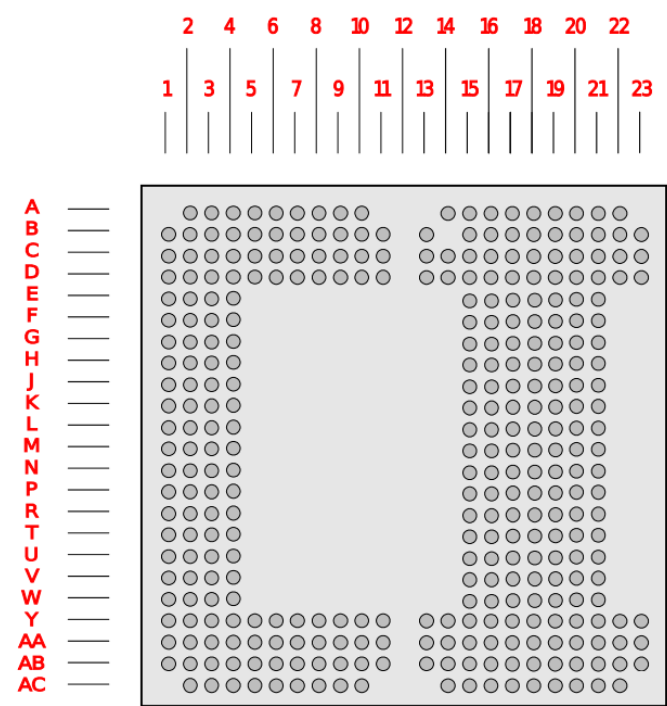


> Module design in scale 1:1:



## 2.6. byteENGINE AM62L OSM-S interface

The byteENGINE **AM62L** is soldered to the carrier board with 332 contacts. The following picture shows the location of the solder balls on the bottom side of the SOM byteENGINE AM62L.



<https://sget.org>



**OPEN  
STANDARD  
MODULE™**

## 2.7. TI SYSCONFIG

TI SysConfig is a configuration and code-generation tool from Texas Instruments that simplifies the setup of hardware and software for TI microcontrollers and processors. It provides an intuitive graphical user interface that allows developers to configure device features without manual register-level programming, reducing development effort and the risk of configuration errors. SysConfig supports both device-level and system-level configuration and is well suited for use with TI-based System-on-Module designs.

The tool enables graphical configuration of pin multiplexing (PinMux), on-chip peripherals, clock trees, memory settings, and software components such as drivers, middleware, and RTOS options. It continuously checks the configuration for conflicts, for example overlapping pin assignments or invalid clock settings, and guides the user toward valid solutions. This helps ensure a consistent and reliable system configuration early in the development process.

Based on the selected configuration, SysConfig automatically generates initialization code and related

configuration files that can be directly integrated into the application firmware. This reduces handwritten boilerplate code and ensures that hardware and software initialization matches the configured system. The tool also provides contextual help and real-time previews of the generated code, improving transparency and traceability.

SysConfig can be used as an online tool. It supports typical use cases such as configuring GPIOs, UART, I2C, SPI, timers, system clocks, memory interfaces, and RTOS settings, making it a key tool for efficient and reliable development of TI CPU-based System-on-Module solutions.



[TI SYSCONFIG Tool](#)

## 3. Pinout functions

### 3.1. Restrictions

These peripherals are internally connected and should not be utilized in the customer design if the components are populated and in use.


**WARNING:**

EEPROM: Production data storage, do not use for application, readonly usage.

#### Restricted peripherals

**WKUP\_I2C0**      EEPROM Addr. 0x50, PMIC Addr. 0x30

**MMC0**            eMMC

| CPU Ball | Pin Name   | Function   | Used for |
|----------|------------|------------|----------|
| B2       | MMC0_CLK   | MMC0.CLK   | eMMC     |
| D2       | MMC0_CMD   | MMC0.CMD   | eMMC     |
| D3       | MMC0_DATA0 | MMC0.DATA0 | eMMC     |
| D4       | MMC0_DATA1 | MMC0.DATA1 | eMMC     |
| C1       | MMC0_DATA2 | MMC0.DATA2 | eMMC     |
| C2       | MMC0_DATA3 | MMC0.DATA3 | eMMC     |
| C4       | MMC0_DATA4 | MMC0.DATA4 | eMMC     |
| B3       | MMC0_DATA5 | MMC0.DATA5 | eMMC     |
| A3       | MMC0_DATA6 | MMC0.DATA6 | eMMC     |
| B4       | MMC0_DATA7 | MMC0.DATA7 | eMMC     |

## 3.2. PIN List

| OSM Pin | OSM Pin Name     | MPU Pin | GPIOx/    | Pin -Function / Mode-Mux |              |                  |           |            |          |            |            |   |                           |
|---------|------------------|---------|-----------|--------------------------|--------------|------------------|-----------|------------|----------|------------|------------|---|---------------------------|
|         |                  |         |           | 1                        | 2            | 3                | 4         | 5          | 6        | 7          | 8          | 9 | Details                   |
| A2      | CSLA_DATA1_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A3      | CSLA_DATA1_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A4      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| A5      | CSLA_DATA2_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A6      | CSLA_DATA2_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A7      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| A8      | USB_C_SSTX_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A9      | USB_C_SSTX_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A10     | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| A14     | UART_A_RX        | F22     | UART5_RXD | GPMC0_AD14               | VOUT0_DATA14 | MCASP2_AFSR      | EHRPWM0_B | TRC_DATA20 | GPIO0_29 | UART2_CTSn | BOOTMODE14 |   | BOOT PIN - RESTRICTED USE |
| A15     | CH1_RX_N         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A16     | CH1_LINK         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A17     | CH1_TX_N         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A18     | CH0_SYNC_TRIGGER | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A19     | CH0_RX_N         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A20     | CH0_LINK         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A21     | CH0_TX_N         | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| A22     | UART_C_RX        | C11     | UART1_RXD | MCASP0_AFSR              | SPI2_CS0     | ADC_EXT_TRIGGER0 | EHRPWM0_A | GPIO0_85   | EQEP1_S  |            |            |   |                           |
| B1      | CSLA_DATA0_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B2      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| B3      | CSLA_CLOCK_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B4      | CSLA_CLOCK_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B5      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| B6      | CSLA_DATA3_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B7      | CSLA_DATA3_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B8      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| B9      | GND              | -       | GND       |                          |              |                  |           |            |          |            |            |   |                           |
| B10     | USB_C_SSRX_P     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B11     | USB_C_SSRX_N     | N/A     |           |                          |              |                  |           |            |          |            |            |   |                           |
| B13     | UART_A_TX        | F23     | UART5_TXD | GPMC0_AD15               | VOUT0_DATA15 | MCASP2_ACLKR     | EHRPWM1_A | TRC_DATA19 | GPIO0_30 | UART2_RTSn | BOOTMODE15 |   | BOOT PIN - RESTRICTED USE |

| OSM Pin | OSM Pin Name             | MPU Pin | GPIOx/       | Pin -Function / Mode-Mux |             |                      |           |                       |          |            |   |   | Details |
|---------|--------------------------|---------|--------------|--------------------------|-------------|----------------------|-----------|-----------------------|----------|------------|---|---|---------|
|         |                          |         |              | 1                        | 2           | 3                    | 4         | 5                     | 6        | 7          | 8 | 9 |         |
| B15     | CH1_RX_P                 | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B16     | CH1_ACT                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B17     | CH1_TX_P                 | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B18     | CH1_SYNC_TRIGG<br>ER     | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B19     | CH0_RX_P                 | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B20     | CH0_ACT                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B21     | CH0_TX_P                 | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B22     | Vendor Defined           | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| B23     | UART_C_TX                | A12     | UART1_TXD    | MCASP0_ACLK<br>R         | SPI2_CLK    | ADC_EXT_TRIG<br>GER1 | EHRPWM0_B | GPIO0_86              | EQEP1_I  |            |   |   |         |
| C1      | CSLA_DATA0_N             | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C2      | CAM_MCK                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C3      | CAM_A_SDA /<br>CSLA_TX_N | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C4      | CAM_A_SCL /<br>CSLA_TX_P | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C5      | VCC_6_TEST               | -       | VDD_CORE     |                          |             |                      |           |                       |          |            |   |   |         |
| C6      | ETH_B_MDC                | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C7      | ETH_B_MDIO               | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C8      | USB_C_OC#                | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C9      | USB_C_VBUS               | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C10     | USB_C_EN                 | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C11     | GND                      | -       | GND          |                          |             |                      |           |                       |          |            |   |   |         |
| C13     | UART_A_RTS               | E18     | UART5_RTSn   | OSPI0_LBCLK0             | GPIO0_1     |                      |           |                       |          |            |   |   |         |
| C14     | UART_A_CTS               | E22     | UART5_CTSn   | OSPI0_DQS                | GPIO0_2     |                      |           |                       |          |            |   |   |         |
| C15     | CH1_RXC                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C16     | Vendor Defined           | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C17     | CH1_TXC                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C18     | TEST_GENERIC             | -       | VDDS_RTC     |                          |             |                      |           |                       |          |            |   |   |         |
| C19     | CH0_RXC                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C20     | SDIO_A_IOPWR             | -       | VDDSHV_SD_IO |                          |             |                      |           |                       |          |            |   |   |         |
| C21     | CH0_TXC                  | N/A     |              |                          |             |                      |           |                       |          |            |   |   |         |
| C22     | UART_D_RX                | H19     | UART2_RXD    | GPMC0_AD8                | VOUT0_DATA8 | MCASP2_AXR0          | SPI1_CS2  | MCASP1_AXR4           | GPIO0_23 | BOOTMODE08 |   |   |         |
| C23     | UART_D_TX                | H20     | UART2_TXD    | GPMC0_AD9                | VOUT0_DATA9 | MCASP2_AXR1          | TIMER_I02 | ECAP2_IN_APW<br>M_OUT | GPIO0_24 | BOOTMODE09 |   |   |         |
| D1      | GND                      | -       | GND          |                          |             |                      |           |                       |          |            |   |   |         |

| OSM Pin | OSM Pin Name         | MPU Pin | GPIOx/     | Pin -Function / Mode-Mux |                   |                |                 |             |                         |                    |                   |           | Details                                                   |
|---------|----------------------|---------|------------|--------------------------|-------------------|----------------|-----------------|-------------|-------------------------|--------------------|-------------------|-----------|-----------------------------------------------------------|
|         |                      |         |            | 1                        | 2                 | 3              | 4               | 5           | 6                       | 7                  | 8                 | 9         |                                                           |
| D2      | ETH_B_(R)(G)MII_CRS  | N/A     |            |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D3      | GPIO_C_0             | B13     | GPIO0_95   | UART0_RTSn               | SPI0_CS3          | I2C3_SDA       | UART2_TXD       | TIMER_I03   | AUDIO_EXT_RE FCLK1      | MCAN2_TX           | MCASP2_ACLKX      | MMC2_SDWP |                                                           |
| D4      | GPIO_C_1             | B8      | GPIO0_102  | I2C2_SCL                 |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D5      | GND                  | -       | GND        |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D6      | Vendor Defined       | G22     | BOOTMODE13 |                          |                   |                |                 |             |                         |                    |                   |           | CONNECTED ONLY DURING POR, PULLUP 10K 1V8 (RED. BOOTMODE) |
| D7      | Vendor Defined       | F23     | BOOTMODE15 |                          |                   |                |                 |             |                         |                    |                   |           | CONNECTED ONLY DURING POR, PULLUP 10K 1V8 (RED. BOOTMODE) |
| D8      | GND                  | -       | GND        |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D9      | USB_C_ID             | N/A     |            |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D10     | USB_C_D_P            | N/A     |            |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D11     | USB_C_D_N            | N/A     |            |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D13     | UART_B_TX            | G22     | UART4_TXD  | GPMC0_AD13               | VOUT0_DATA13      | MCASP2_ACLKX   | EHRPWM0_A       | TRC_DATA21  | GPIO0_28                | BOOTMODE13         |                   |           | BOOT PIN - RESTRICTED USE                                 |
| D14     | UART_B_RX            | G23     | UART4_RXD  | GPMC0_AD12               | VOUT0_DATA12      | MCASP2_AFSX    | EHRPWM_TZn_I N2 | TRC_DATA22  | GPIO0_27                | BOOTMODE12         |                   |           | BOOT PIN - RESTRICTED USE                                 |
| D15     | UART_B_RTS           | H23     | UART4_RTSn | GPMC0_AD6                | VOUT0_DATA6       | MCASP2_AXR10   | SPI1_CS3        | TRC_DATA4   | GPIO0_21                | BOOTMODE06         |                   |           |                                                           |
| D16     | UART_B_CTS           | J22     | UART4_CTSn | GPMC0_AD7                | VOUT0_DATA7       | MCASP2_AXR11   | MCASP1_AXR5     | TRC_DATA5   | GPIO0_22                | BOOTMODE07         |                   |           |                                                           |
| D17     | GPIO_A_0             | L23     | GPIO0_16   | GPMC0_AD1                | VOUT0_DATA1       | UART6_TXD      | MCASP2_AXR5     | I2C3_SDA    | ECAP1_IN_APW M_OUT      | TRC_CTL            | BOOTMODE01        |           |                                                           |
| D18     | GND                  | -       | GND        |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| D19     | GPIO_B_0             | L20     | GPIO0_41   | GPMC0_CS0                | VOUT0_VSYNC       | SPI2_D1        | MCASP2_AXR14    | EQEP1_S     | TRC_DATA15              |                    |                   |           |                                                           |
| D20     | SDIO_A_WP            | D6      | MMC1_SDWP  | UART6_TXD                | TIMER_I03         | UART3_CTSn     | MCAN2_TX        | SPI3_CS1    | GPIO0_123               |                    |                   |           |                                                           |
| D21     | SDIO_A_PWR_EN        | C23     | GPIO0_14   | OSPI0_CS0                | OSPI0_RESET_ OUT0 | OSPI0_ECC_FAIL | MCASP1_ACLKR    | MCASP1_AXR3 | UART5_TXD               | ADC_EXT_TRIG GER1  |                   |           |                                                           |
| D22     | UART_CON_RX          | D13     | UART0_RXD  | ECAP1_IN_AP WM_OUT       | SPI2_D0           | EHRPWM2_A      | GPIO0_92        |             |                         |                    |                   |           |                                                           |
| D23     | UART_CON_TX          | C13     | UART0_TXD  | ECAP2_IN_AP WM_OUT       | SPI2_D1           | EHRPWM2_B      | GPIO0_93        |             |                         |                    |                   |           |                                                           |
| E1      | ETH_B_(R)(G)MII_C OL | N/A     |            |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| E2      | GND                  | -       | GND        |                          |                   |                |                 |             |                         |                    |                   |           |                                                           |
| E3      | GPIO_C_2/ CAM_B_PWR  | D8      | GPIO0_103  | I2C2_SDA                 |                   |                |                 |             |                         |                    |                   |           |                                                           |
| E4      | GPIO_C_3/ CAM_B_RST# | D16     | GPIO0_104  | EXT_REFCLK1              | SYNC1_OUT         | SPI2_CS3       | TIMER_I00       | CLKOUT0     | CP_GEMAC_CP TS0_RFT_CLK | ECAP0_IN_APW M_OUT | ADC_EXT_TRIGG ER0 |           |                                                           |

| OSM Pin | OSM Pin Name             | MPU Pin | GPIOx/          | Pin -Function / Mode-Mux |                         |                   |                   |                  |                |            |             |   |         |
|---------|--------------------------|---------|-----------------|--------------------------|-------------------------|-------------------|-------------------|------------------|----------------|------------|-------------|---|---------|
|         |                          |         |                 | 1                        | 2                       | 3                 | 4                 | 5                | 6              | 7          | 8           | 9 | Details |
| E15     | GND                      | -       | GND             |                          |                         |                   |                   |                  |                |            |             |   |         |
| E16     | ETH_A_(R)(G)MII_CRS      | N/A     |                 |                          |                         |                   |                   |                  |                |            |             |   |         |
| E17     | GPIO_A_1                 | H21     | GPIO0_25        | GPMC0_AD10               | VOUT0_DATA10            | UART3_RXD         | MCASP2_AXR2       | EHRPWM0_SYNCI    | OBSCCLK0       | BOOTMODE10 |             |   |         |
| E18     | PWM_0/<br>DISP_BL_PWM    | E11     | EHRPWM0_A       | SPI0_CS0                 | GPIO0_87                |                   |                   |                  |                |            |             |   |         |
| E19     | GPIO_B_1                 | L19     | GPIO0_42        | GPMC0_CSn1               | VOUT0_PCLK              | SPI2_CS0          | MCASP2_AXR15      | EQEP1_I          | TRC_DATA16     |            |             |   |         |
| E20     | SDIO_A_CMD               | Y3      | MMC1_CMD        | TIMER_I01                | UART3_TXD               | SPI3_CLK          | SPI2_CS0          | GPIO0_121        |                |            |             |   |         |
| E21     | GND                      | -       | GND             |                          |                         |                   |                   |                  |                |            |             |   |         |
| F1      | ETH_B_(S)(R)(G)MII_TXD1  | AB13    | RGMII2_TD1      | RMII2_TXD1               | MCASP2_ACLK_R           | MCASP2_AXR8       | GPIO0_70          |                  |                |            |             |   |         |
| F2      | ETH_B_(S)(R)(G)MII_TXD3  | AA13    | RGMII2_TD3      | GPMC0_A2                 | MCASP2_ACLK_X           | SPI3_D1           | GPIO0_72          | EQEP2_S          |                |            |             |   |         |
| F3      | GPIO_C_4/<br>DISP_VDD_EN | W22     | WKUP_UART0_RTSn | WKUP_TIMER_I01           | WKUP_EXT_REFCLK0        | WKUP_GPIO0_3      |                   |                  |                |            |             |   |         |
| F4      | GPIO_C_5/<br>DISP_BL_EN  | W23     | WKUP_UART0_CTSn | WKUP_TIMER_I00           | WKUP_OBSCCLK0           | WKUP_SYSCCLK_OUT0 | WKUP_GPIO0_2      |                  |                |            |             |   |         |
| F15     | ETH_A_(R)(G)MII_C0L      | N/A     |                 |                          |                         |                   |                   |                  |                |            |             |   |         |
| F16     | GND                      | -       | GND             |                          |                         |                   |                   |                  |                |            |             |   |         |
| F17     | GPIO_A_2                 | H18     | GPIO0_26        | GPMC0_AD11               | VOUT0_DATA11            | UART3_TXD         | MCASP2_AXR3       | EHRPWM0_SYNC0    | TRC_DATA23     | BOOTMODE11 |             |   |         |
| F18     | PWM_1                    | D11     | EHRPWM0_B       | SPI0_CS1                 | CP_GEMAC_CP_TS0_TS_COMP | ECAP0_IN_APWM_OUT | AUDIO_EXT_REFCLK1 | GPIO0_88         | EHRPWM_TZn_IN5 |            |             |   |         |
| F19     | GPIO_B_2                 | M23     | GPIO0_43        | GPMC0_CSn2               | I2C2_SCL                | MCASP1_AXR4       | UART4_RXD         | ADC_EXT_TRIGGER0 | VOUT0_EXTCLKIN | TRC_DATA17 | MCASP1_AFSR |   |         |
| F20     | GND                      | -       | GND             |                          |                         |                   |                   |                  |                |            |             |   |         |
| F21     | SDIO_A_CLK               | Y2      | MMC1_CLK        | TIMER_I00                | UART3_RXD               | SPI3_CS0          | SPI2_CS2          | GPIO0_120        |                |            |             |   |         |
| G1      | ETH_B_(S)(R)(G)MII_TXD0  | AC12    | RGMII2_TD0      | RMII2_TXD0               | MCASP2_AXR6             | GPIO0_69          |                   |                  |                |            |             |   |         |
| G2      | ETH_B_(S)(R)(G)MII_TXD2  | AA12    | RGMII2_TD2      | GPMC0_A1                 | MCASP2_AFSX             | SPI3_D0           | GPIO0_71          | EQEP2_I          |                |            |             |   |         |
| G3      | GPIO_C_6/<br>CAM_A_PWR   | Y22     | WKUP_UART0_RXD  | WKUP_GPIO0_0             |                         |                   |                   |                  |                |            |             |   |         |
| G4      | GPIO_C_7/<br>CAM_A_RST#  | AA23    | WKUP_UART0_TXD  | WKUP_GPIO0_1             |                         |                   |                   |                  |                |            |             |   |         |
| G15     | ETH_A_(S)(R)(G)MII_TXD1  | W13     | RGMII1_TD1      | RMII1_TXD1               | GPIO0_56                |                   |                   |                  |                |            |             |   |         |
| G16     | ETH_A_(S)(R)(G)MII_TXD3  | AA11    | RGMII1_TD3      | CLKOUT0                  | GPIO0_58                |                   |                   |                  |                |            |             |   |         |
| G17     | GPIO_A_3                 | N19     | GPIO0_32        | GPMC0_ADVn_ALE           | VOUT0_DATA17            | MCASP1_AXR2       | EHRPWM_TZn_IN1    | SPI3_CS3         | TRC_DATA7      |            |             |   |         |

| OSM Pin | OSM Pin Name               | MPU Pin | GPIOx/        | Pin -Function / Mode-Mux     |                             |              |                       |                       |                      |            |              |   |                |
|---------|----------------------------|---------|---------------|------------------------------|-----------------------------|--------------|-----------------------|-----------------------|----------------------|------------|--------------|---|----------------|
|         |                            |         |               | 1                            | 2                           | 3            | 4                     | 5                     | 6                    | 7          | 8            | 9 | Details        |
| G18     | PWM_2                      | E13     | EHRPWM1_A     | SPI0_CLK                     | CP_GEMAC_CPT<br>TS0_TS_SYNC | GPIO0_89     |                       |                       |                      |            |              |   |                |
| G19     | GPIO_B_3                   | M22     | GPIO0_44      | GPMC0_CSn3                   | I2C2_SDA                    | WKUP_CLKOUT0 | UART4_TXD             | MCASP1_AXR5           | ADC_EXT_TRIG<br>GER1 | TRC_DATA18 | MCASP1_ACLKR |   |                |
| G20     | SDIO_A_D0                  | AA1     | MMC1_DAT0     | CP_GEMAC_CPT<br>S0_HW2TSPUSH | TIMER_I03                   | UART2_CTSn   | ECAP2_IN_APW<br>M_OUT | SPI2_D1               | GPIO0_119            |            |              |   |                |
| G21     | SDIO_A_D1                  | Y4      | MMC1_DAT1     | CP_GEMAC_CPT<br>S0_HW1TSPUSH | TIMER_I02                   | UART2_RTSn   | ECAP1_IN_APW<br>M_OUT | SPI3_CS2              | SPI2_D0              | GPIO0_118  |              |   |                |
| H1      | ETH_B_(R)(G)MII_TX_CLK     | Y13     | RGMII2_TXC    | RMII2_CRSDV                  | MCASP2_AXR5                 | GPIO0_68     |                       |                       |                      |            |              |   |                |
| H2      | GND                        | -       | GND           |                              |                             |              |                       |                       |                      |            |              |   |                |
| H3      | RGB_CS#                    | AB19    | EXT_WAKEUP0   |                              |                             |              |                       |                       |                      |            |              |   |                |
| H4      | GND                        | -       | GND           |                              |                             |              |                       |                       |                      |            |              |   |                |
| H15     | ETH_A_(S)(R)(G)MII_TXD0    | AC10    | RGMII1_TD0    | RMII1_TXD0                   | GPIO0_55                    |              |                       |                       |                      |            |              |   |                |
| H16     | ETH_A_(S)(R)(G)MII_TXD2    | Y11     | RGMII1_TD2    | GPMC0_A0                     | GPIO0_57                    |              |                       |                       |                      |            |              |   |                |
| H17     | GPIO_A_4                   | P23     | GPIO0_35      | GPMC0_BE0n_CLE               | VOUT0_DATA20                | MCASP1_ACLKX | EQEP0_A               | SPI3_CS0              | TRC_DATA10           |            |              |   |                |
| H18     | PWM_3                      | L21     | EHRPWM1_B     | GPMC0_CLK                    | VOUT0_DATA16                | MCASP1_AXR3  | GPMC0_FCLK_MUX        | TRC_DATA6             | GPIO0_31             |            |              |   |                |
| H19     | GPIO_B_4                   | A8      | GPIO0_79      | MCASP0_AXR3                  | SPI2_D0                     | UART1_CTSn   | UART6_RXD             | ECAP1_IN_APW<br>M_OUT | MCAN1_RX             | EQEP0_A    |              |   |                |
| H20     | SDIO_A_D2                  | AA2     | MMC1_DAT2     | CP_GEMAC_CPT<br>TS0_TS_SYNC  | TIMER_I01                   | UART2_TXD    | MCAN1_TX              | SPI3_D1               | SPI2_CS3             | GPIO0_117  |              |   |                |
| H21     | SDIO_A_D3                  | AB2     | MMC1_DAT3     | CP_GEMAC_CPT<br>TS0_TS_COMP  | TIMER_I00                   | UART2_RXD    | MCAN1_RX              | SPI3_D0               | SPI2_CS1             | GPIO0_116  |              |   |                |
| J1      | ETH_B_(S)(R)(G)MII_RXD0    | AB9     | RGMII2_RD0    | RMII2_RXD0                   | MCASP2_AXR2                 | GPIO0_75     |                       |                       |                      |            |              |   |                |
| J2      | ETH_B_(R)(G)MII_TX_EN(_ER) | AB12    | RGMII2_TX_CTL | RMII2_TX_EN                  | MCASP2_AXR4                 | GPIO0_67     |                       |                       |                      |            |              |   |                |
| J3      | RGB_RESET#                 | AA16    | EMU1          |                              |                             |              |                       |                       |                      |            |              |   | PULLUP 10K 1V8 |
| J4      | RGB_DE                     | V21     | ADC0_AIN3     |                              |                             |              |                       |                       |                      |            |              |   |                |
| J15     | ETH_A_(R)(G)MII_TX_CLK     | W11     | RGMII1_TXC    | RMII1_CRSDV                  | GPIO0_54                    |              |                       |                       |                      |            |              |   |                |
| J16     | GND                        | -       | GND           |                              |                             |              |                       |                       |                      |            |              |   |                |
| J17     | GPIO_A_5                   | P22     | GPIO0_36      | GPMC0_BE1n                   | VOUT0_DATA21                | MCASP2_AXR12 | EQEP0_B               | SPI3_CLK              | TRC_DATA11           |            |              |   |                |
| J18     | PWM_4                      | N20     | EHRPWM2_A     | GPMC0_OEn_R<br>En            | VOUT0_DATA18                | MCASP1_AXR1  | SPI3_CS2              | TRC_DATA8             | GPIO0_33             |            |              |   |                |
| J19     | GPIO_B_5                   | B10     | GPIO0_80      | MCASP0_AXR2                  | SPI2_D1                     | UART1_RTSn   | UART6_TXD             | ECAP2_IN_APW<br>M_OUT | MCAN1_TX             | EQEP0_B    |              |   |                |
| J20     | GND                        | -       | GND           |                              |                             |              |                       |                       |                      |            |              |   |                |
| J21     | SDIO_A_CD#                 | B6      | MMC1_SD_CD    | UART6_RXD                    | TIMER_I02                   | UART3_RTSn   | MCAN2_RX              | SPI3_CS3              | SPI2_CLK             | GPIO0_122  |              |   |                |

| OSM Pin | OSM Pin Name               | MPU Pin | GPIOx/        | Pin -Function / Mode-Mux |                           |                |           |           |                    |            |   |   |                |
|---------|----------------------------|---------|---------------|--------------------------|---------------------------|----------------|-----------|-----------|--------------------|------------|---|---|----------------|
|         |                            |         |               | 1                        | 2                         | 3              | 4         | 5         | 6                  | 7          | 8 | 9 | Details        |
| K1      | ETH_B_(S)(R)(G)MII_RXD1    | AC9     | RGMII2_RD1    | RMII2_RXD1               | MCASP2_AFSR               | MCASP2_AXR7    | GPIO0_76  |           |                    |            |   |   |                |
| K2      | ETH_B_(R)(G)MII_RX_ER      | N/A     |               |                          |                           |                |           |           |                    |            |   |   |                |
| K3      | RGB_HSYNC                  | N/A     |               |                          |                           |                |           |           |                    |            |   |   |                |
| K4      | RGB_DISP                   | AB20    | EXT_WAKEUP1   |                          |                           |                |           |           |                    |            |   |   |                |
| K15     | ETH_A_(S)(R)(G)MII_RXD0    | Y8      | RGMII1_RD0    | RMII1_RXD0               | GPIO0_61                  |                |           |           |                    |            |   |   |                |
| K16     | ETH_A_(R)(G)MII_TX_EN(_ER) | AB11    | RGMII1_TX_CTL | RMII1_TX_EN              | GPIO0_53                  |                |           |           |                    |            |   |   |                |
| K17     | GPIO_A_6                   | N23     | GPIO0_37      | GPMC0_WAIT0              | VOUT0_DATA22              | MCASP1_AFSX    | EQEP0_S   | SPI3_D0   | TRC_DATA12         |            |   |   |                |
| K18     | PWM_5                      | M19     | EHRPWM2_B     | GPMC0_WEn                | VOUT0_DATA19              | MCASP1_AXR0    | SPI3_CS1  | TRC_DATA9 | GPIO0_34           |            |   |   |                |
| K19     | GPIO_B_6                   | E12     | GPIO0_90      | SPI0_D0                  | CP_GEMAC_CPT_S0_HW1TSPUSH | EHRPWM1_B      |           |           |                    |            |   |   |                |
| K20     | SDIO_B_CLK                 | R23     | MMC2_CLK      | MCASP1_ACLKR             | MCASP1_AXR5               | UART6_RXD      | GPIO0_49  |           |                    |            |   |   |                |
| K21     | SDIO_B_CMD                 | U23     | MMC2_CMD      | MCASP1_AFSR              | MCASP1_AXR4               | UART6_TXD      | GPIO0_50  |           |                    |            |   |   |                |
| L1      | ETH_B_(R)(G)MII_RX_DV(_ER) | AC8     | RGMII2_RX_CTL | RMII2_RX_ER              | MCASP2_AXR3               | GPIO0_73       |           |           |                    |            |   |   |                |
| L2      | GND                        | -       | GND           |                          |                           |                |           |           |                    |            |   |   |                |
| L3      | RGB_VSYNC                  | V23     | ADC0_AIN2     |                          |                           |                |           |           |                    |            |   |   |                |
| L4      | GND                        | -       | GND           |                          |                           |                |           |           |                    |            |   |   |                |
| L15     | ETH_A_(S)(R)(G)MII_RXD1    | AA6     | RGMII1_RD1    | RMII1_RXD1               | GPIO0_62                  |                |           |           |                    |            |   |   |                |
| L16     | ETH_A_(R)(G)MII_RX_ER      | N/A     |               |                          |                           |                |           |           |                    |            |   |   |                |
| L17     | GPIO_A_7                   | N21     | GPIO0_39      | GPMC0_WPn                | VOUT0_HSYNC               | SPI2_CLK       | UART6_TXD | EQEP1_A   | AUDIO_EXT_RE_FCLK1 | TRC_DATA13 |   |   |                |
| L18     | GND                        | -       | GND           |                          |                           |                |           |           |                    |            |   |   |                |
| L19     | GPIO_B_7                   | B12     | GPIO0_91      | SPI0_D1                  | CP_GEMAC_CPT_S0_HW2TSPUSH | EHRPWM_TZn_IN0 |           |           |                    |            |   |   |                |
| L20     | SDIO_B_D0                  | U22     | MMC2_DAT0     | MCASP1_AXR0              | GPIO0_48                  |                |           |           |                    |            |   |   |                |
| L21     | SDIO_B_D1                  | T22     | MMC2_DAT1     | MCASP1_AXR1              | GPIO0_47                  |                |           |           |                    |            |   |   |                |
| M1      | ETH_B_(R)(G)MII_RXD2       | AB10    | RGMII2_RD2    | GPMC0_A3                 | MCASP2_AXR0               | SPI3_CLK       | GPIO0_77  | EQEP2_A   |                    |            |   |   |                |
| M2      | ETH_B_SDP                  | N/A     |               |                          |                           |                |           |           |                    |            |   |   |                |
| M3      | RGB_B5                     | N/A     |               |                          |                           |                |           |           |                    |            |   |   |                |
| M4      | RGB_(PIXEL)CLK             | Y16     | EMU0          |                          |                           |                |           |           |                    |            |   |   | PULLUP 10K 1V8 |
| M15     | ETH_A_(R)(G)MII_RX_DV(_ER) | Y6      | RGMII1_RX_CTL | RMII1_RX_ER              | GPIO0_59                  |                |           |           |                    |            |   |   |                |
| M16     | GND                        | -       | GND           |                          |                           |                |           |           |                    |            |   |   |                |
| M17     | ETH_IOPWR                  | -       | 1V8           |                          |                           |                |           |           |                    |            |   |   |                |

| OSM Pin | OSM Pin Name               | MPU Pin | GPIOx/     | Pin -Function / Mode-Mux |                       |          |          |         |   |   |   |   | Details        |
|---------|----------------------------|---------|------------|--------------------------|-----------------------|----------|----------|---------|---|---|---|---|----------------|
|         |                            |         |            | 1                        | 2                     | 3        | 4        | 5       | 6 | 7 | 8 | 9 |                |
| M18     | ADC_0                      | V20     | ADC0_AIN0  |                          |                       |          |          |         |   |   |   |   |                |
| M19     | VCC_2_TEST                 | -       | VDDA_1V8   |                          |                       |          |          |         |   |   |   |   |                |
| M20     | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| M21     | SDIO_B_D2                  | T23     | MMC2_DAT2  | MCASP1_AXR2              | UART5_TXD             | GPIO0_46 |          |         |   |   |   |   |                |
| N1      | ETH_B_(R)(G)MII_R<br>XD3   | AB8     | RGMII2_RD3 | GPMC0_A4                 | AUDIO_EXT_RE<br>FCLK0 | SPI3_CS0 | GPIO0_78 | EQEP2_B |   |   |   |   |                |
| N2      | RESERVED                   | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| N3      | RGB_B3                     | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| N4      | RGB_B4                     | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| N15     | ETH_A_(R)(G)MII_R<br>XD2   | AA8     | RGMII1_RD2 | I2C2_SCL                 | GPMC0_A5              | GPIO0_63 |          |         |   |   |   |   |                |
| N16     | ETH_A_SDP                  | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| N17     | JTAG_TCK(SWCLK)            | AB14    | TCK        |                          |                       |          |          |         |   |   |   |   | PULLUP 10K 1V8 |
| N18     | ADC_1                      | V22     | ADC0_AIN1  |                          |                       |          |          |         |   |   |   |   |                |
| N19     | JTAG_TMS(SWDIO)            | Y17     | TMS        |                          |                       |          |          |         |   |   |   |   | PULLUP 10K 1V8 |
| N20     | SDIO_B_D3                  | R22     | MMC2_DAT3  | MCASP1_AXR3              | UART5_RXD             | GPIO0_45 |          |         |   |   |   |   |                |
| N21     | SDIO_B_D4                  | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| P1      | ETH_B_(R)(G)MII_R<br>X_CLK | AC7     | RGMII2_RXC | RMII2_REF_CLK            | MCASP2_AXR1           | GPIO0_74 |          |         |   |   |   |   |                |
| P2      | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| P3      | RGB_B2                     | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| P4      | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| P15     | ETH_A_(R)(G)MII_R<br>XD3   | W8      | RGMII1_RD3 | I2C2_SDA                 | GPMC0_A6              | GPIO0_64 |          |         |   |   |   |   |                |
| P16     | Vendor Defined             | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| P17     | JTAG_TDI                   | AC16    | TDI        |                          |                       |          |          |         |   |   |   |   | PULLUP 10K 1V8 |
| P18     | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| P19     | JTAG_RTCK                  | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| P20     | SDIO_B_D5                  | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| P21     | SDIO_B_D6                  | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| R1      | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| R2      | PCle_SM_ALERT#             | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| R3      | RGB_B1                     | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| R4      | RGB_B0                     | N/A     |            |                          |                       |          |          |         |   |   |   |   |                |
| R15     | ETH_A_(R)(G)MII_R<br>X_CLK | Y7      | RGMII1_RXC | RMII1_REF_CLK            | GPIO0_60              |          |          |         |   |   |   |   |                |
| R16     | GND                        | -       | GND        |                          |                       |          |          |         |   |   |   |   |                |
| R17     | JTAG_TDO(SWO)              | AB15    | TDO        |                          |                       |          |          |         |   |   |   |   |                |

| OSM Pin | OSM Pin Name    | MPU Pin | GPIOx/       | Pin -Function / Mode-Mux |             |            |             |           |                    |         |            |   | Details                                                   |
|---------|-----------------|---------|--------------|--------------------------|-------------|------------|-------------|-----------|--------------------|---------|------------|---|-----------------------------------------------------------|
|         |                 |         |              | 1                        | 2           | 3          | 4           | 5         | 6                  | 7       | 8          | 9 |                                                           |
| R18     | BOOT_SEL1#      | F22     | BOOTMODE14   |                          |             |            |             |           |                    |         |            |   | CONNECTED ONLY DURING POR, PULLUP 10K 1V8 (RED. BOOTMODE) |
| R19     | JTAG_nTRST      | AB16    | TRSTn        |                          |             |            |             |           |                    |         |            |   | PULLDOWN 4K7                                              |
| R20     | GND             | -       | GND          |                          |             |            |             |           |                    |         |            |   |                                                           |
| R21     | SDIO_B_D7       | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| T1      | PCIe_SMCLK      | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| T2      | PCIe_WAKE#      | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| T3      | RGB_G4          | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| T4      | RGB_G5          | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| T15     | ETH_MDIO        | AC13    | MDIO0_MDIO   | GPIO0_65                 |             |            |             |           |                    |         |            |   |                                                           |
| T16     | ETH_MDC         | AC15    | MDIO0_MDC    | GPIO0_66                 |             |            |             |           |                    |         |            |   |                                                           |
| T17     | FORCE_RECOVERY# | -       |              |                          |             |            |             |           |                    |         |            |   | PULLUP 10K 1V8                                            |
| T18     | I2S_B_LRCLK     | F20     | MCASP1_AFSX  | OSPI0_D7                 | SPI1_D1     | UART6_CTSn | I2C3_SDA    | UART4_TXD | GPIO0_10           |         |            |   |                                                           |
| T19     | I2S_B_BITCLK    | G20     | MCASP1_ACLKX | OSPI0_D6                 | SPI1_D0     | UART6_RTSn | I2C3_SCL    | UART4_RXD | GPIO0_9            |         |            |   |                                                           |
| T20     | SDIO_B_IOPWR    | -       | 1V8          |                          |             |            |             |           |                    |         |            |   |                                                           |
| T21     | SDIO_B_CD#      | T20     | MMC2_SDCD    | MCASP1_ACLKX             | UART4_RXD   | GPIO0_51   |             |           |                    |         |            |   |                                                           |
| U1      | PCIe_SMDAT      | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| U2      | GND             | -       | GND          |                          |             |            |             |           |                    |         |            |   |                                                           |
| U3      | RGB_G3          | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| U4      | GND             | -       | GND          |                          |             |            |             |           |                    |         |            |   |                                                           |
| U15     | SPI_A_SDI_(IO1) | D21     | OSPI0_D1     | GPIO0_4                  |             |            |             |           |                    |         |            |   |                                                           |
| U16     | SPI_A_SCK       | D22     | OSPI0_CLK    | GPIO0_0                  |             |            |             |           |                    |         |            |   |                                                           |
| U17     | RESET_IN#       | E16     | RESETz       |                          |             |            |             |           |                    |         |            |   | VIA BUFFER                                                |
| U18     | VCC_OUT_IO      | -       | 1V8          |                          |             |            |             |           |                    |         |            |   |                                                           |
| U19     | BOOT_SEL0#      | G23     | BOOTMODE12   |                          |             |            |             |           |                    |         |            |   | CONNECTED ONLY DURING POR, PULLUP 10K 1V8 (RED. BOOTMODE) |
| U20     | SDIO_B_WP       | T21     | MMC2_SDWP    | MCASP1_AFSX              | UART4_TXD   | GPIO0_52   |             |           |                    |         |            |   |                                                           |
| U21     | SDIO_B_PWR_EN   | L22     | GPIO0_15     | GPMC0_AD0                | VOUT0_DATA0 | UART6_RXD  | MCASP2_AXR4 | I2C3_SCL  | ECAP0_IN_APW M_OUT | TRC_CLK | BOOTMODE00 |   |                                                           |
| V1      | GND             | -       | GND          |                          |             |            |             |           |                    |         |            |   |                                                           |
| V2      | PCIe_A_PERST#   | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| V3      | RGB_G1          | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| V4      | RGB_G2          | N/A     |              |                          |             |            |             |           |                    |         |            |   |                                                           |
| V15     | SPI_A_SDO_(IO0) | C22     | OSPI0_D0     | GPIO0_3                  |             |            |             |           |                    |         |            |   |                                                           |
| V16     | GND             | -       | GND          |                          |             |            |             |           |                    |         |            |   |                                                           |

| OSM Pin | OSM Pin Name     | MPU Pin | GPIOx/             | Pin -Function / Mode-Mux |                    |           |           |           |          |          |             |           | Details      |
|---------|------------------|---------|--------------------|--------------------------|--------------------|-----------|-----------|-----------|----------|----------|-------------|-----------|--------------|
|         |                  |         |                    | 1                        | 2                  | 3         | 4         | 5         | 6        | 7        | 8           | 9         |              |
| V17     | CARRIER_PWR_EN   | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| V18     | I2S_MCLK         | B14     | AUDIO_EXT_RE FCLK0 | UART0_CTSn               | SPI0_CS2           | I2C3_SCL  | UART2_RXD | TIMER_IO2 | MCAN2_RX | GPIO0_94 | MCASP2_AFSX | MMC2_SDCD |              |
| V19     | I2S_B_DATA_IN    | F21     | MCASP1_AXR1        | OSPI0_D4                 | SPI1_CS0           | UART6_RXD | GPIO0_7   |           |          |          |             |           |              |
| V20     | GND              | -       | GND                |                          |                    |           |           |           |          |          |             |           |              |
| V21     | I2S_A_DATA_IN    | A9      | MCASP0_AXR1        | SPI2_CS2                 | ECAP1_IN_APW M_OUT | EHRPWM1_A | GPIO0_81  | EQEP0_S   |          |          |             |           |              |
| W1      | PCIe_REFCLK_P    | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| W2      | PCIe_CLKREQ#     | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| W3      | GND              | -       | GND                |                          |                    |           |           |           |          |          |             |           |              |
| W4      | RGB_G0           | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| W15     | SPI_A_HOLD_(IO3) | D23     | OSPI0_D3           | GPIO0_6                  |                    |           |           |           |          |          |             |           |              |
| W16     | SPI_A_WP_(IO2)   | E23     | OSPI0_D2           | GPIO0_5                  |                    |           |           |           |          |          |             |           |              |
| W17     | RTC_PWR          | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| W18     | I2S_A_LRCLK      | B11     | MCASP0_AFSX        | SPI2_CS3                 | AUDIO_EXT_RE FCLK1 | GPIO0_84  | EQEP1_B   |           |          |          |             |           |              |
| W19     | I2S_B_DATA_OUT   | F19     | MCASP1_AXR0        | OSPI0_D5                 | SPI1_CLK           | UART6_TXD | GPIO0_8   |           |          |          |             |           |              |
| W20     | I2S_A_BITCLK     | A11     | MCASP0_ACLK X      | SPI2_CS1                 | ECAP2_IN_APW M_OUT | GPIO0_83  | EQEP1_A   |           |          |          |             |           |              |
| W21     | I2S_A_DATA_OUT   | B9      | MCASP0_AXR0        | AUDIO_EXT_RE FCLK0       | EHRPWM1_B          | GPIO0_82  | EQEP0_I   |           |          |          |             |           |              |
| Y1      | PCIe_REFCLK_N    | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y2      | GND              | -       | GND                |                          |                    |           |           |           |          |          |             |           |              |
| Y3      | VCC_5_TEST       | -       | 3V3                |                          |                    |           |           |           |          |          |             |           |              |
| Y4      | RGB_R5           | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y5      | RGB_R4           | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y6      | RGB_R2           | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y7      | RGB_R0           | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y8      | VCC_IN_5V        | -       | VCC_IN_5V          |                          |                    |           |           |           |          |          |             |           |              |
| Y9      | VCC_IN_5V        | -       | VCC_IN_5V          |                          |                    |           |           |           |          |          |             |           |              |
| Y10     | VCC_IN_5V        | -       | VCC_IN_5V          |                          |                    |           |           |           |          |          |             |           |              |
| Y11     | VCC_IN_5V        | -       | VCC_IN_5V          |                          |                    |           |           |           |          |          |             |           |              |
| Y13     | CARRIER_STBY#    | N/A     |                    |                          |                    |           |           |           |          |          |             |           |              |
| Y14     | RESET_OUT#       | C16     | RESETSTATz         |                          |                    |           |           |           |          |          |             |           | PULLDOWN 10K |
| Y15     | SPI_A_CS0#       | C20     | OSPI0_CSn0         | GPIO0_11                 |                    |           |           |           |          |          |             |           |              |
| Y16     | VCC_3_TEST       | -       | VPP                |                          |                    |           |           |           |          |          |             |           |              |
| Y17     | VCC_IN_5V        | -       | VCC_IN_5V          |                          |                    |           |           |           |          |          |             |           |              |
| Y18     | GND              | -       | GND                |                          |                    |           |           |           |          |          |             |           |              |

| OSM Pin | OSM Pin Name  | MPU Pin | GPIOx/       | Pin -Function / Mode-Mux |             |             |               |             |           |                   |          |   |                           |
|---------|---------------|---------|--------------|--------------------------|-------------|-------------|---------------|-------------|-----------|-------------------|----------|---|---------------------------|
|         |               |         |              | 1                        | 2           | 3           | 4             | 5           | 6         | 7                 | 8        | 9 | Details                   |
| Y19     | VCC_IN_3V3    | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| Y20     | VCC_4_TEST    | -       | VDD_DDR      |                          |             |             |               |             |           |                   |          |   |                           |
| Y21     | SPI_B_SCK     | H22     | SPI1_CLK     | GPMC0_AD5                | VOUT0_DATA5 | UART5_CTSn  | MCASP2_AXR9   | TRC_DATA3   | GPIO0_20  | BOOTMODE05        |          |   |                           |
| Y22     | SPI_B_SDI     | K22     | SPI1_D0      | GPMC0_AD2                | VOUT0_DATA2 | UART6_RTSn  | MCASP2_AXR6   | TRC_DATA0   | GPIO0_17  | BOOTMODE02        |          |   |                           |
| Y23     | SPI_B_SDO     | J23     | SPI1_D1      | GPMC0_AD3                | VOUT0_DATA3 | UART6_CTSn  | MCASP2_AXR7   | TRC_DATA1   | GPIO0_18  | BOOTMODE03        |          |   |                           |
| AA1     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA2     | RESERVED      | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA3     | DSI_TE        | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA4     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA5     | RGB_R3        | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA6     | RGB_R1        | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA7     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA8     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA9     | PWR_BTN#      | AA18    | PMIC_LPM_EN0 |                          |             |             |               |             |           |                   |          |   | VIA OR TO CPU AND PMIC_EN |
| AA10    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA11    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA13    | RESERVED      | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA14    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA15    | I2C_A_SCL     | B7      | I2C0_SCL     | SYNC0_OUT                | OBCLK1      | UART1_DCDn  | EQEP2_A       | EHRPWM_SOCA | GPIO0_98  | ECAP1_IN_APWM_OUT | SPI2_CS0 |   |                           |
| AA16    | I2C_A_SDA     | A7      | I2C0_SDA     | SPI2_CS2                 | TIMER_I01   | UART1_DSRRn | EQEP2_B       | EHRPWM_SOCB | GPIO0_99  | ECAP2_IN_APWM_OUT |          |   |                           |
| AA17    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA18    | V_BAT         | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AA19    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA20    | I2C_B_SCL     | D7      | I2C1_SCL     | UART1_RXD                | TIMER_I00   | SPI2_CS1    | EHRPWM0_SYNCI | GPIO0_100   | EHRPWM2_A | MMC2_SDCCD        |          |   |                           |
| AA21    | I2C_B_SDA     | A6      | I2C1_SDA     | UART1_TXD                | TIMER_I01   | SPI2_CLK    | EHRPWM0_SYNCO | GPIO0_101   | EHRPWM2_B | MMC2_SDWP         |          |   |                           |
| AA22    | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AA23    | SPI_B_CS0#    | K23     | SPI1_CS0     | GPMC0_AD4                | VOUT0_DATA4 | UART5_RTSn  | MCASP2_AXR8   | TRC_DATA2   | GPIO0_19  | BOOTMODE04        |          |   |                           |
| AB1     | PCIe_A_HSI0_P | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AB2     | PCIe_A_HSI0_N | N/A     |              |                          |             |             |               |             |           |                   |          |   |                           |
| AB3     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AB4     | DSI_DATA3_P   | B21     | DSI0_TXP3    |                          |             |             |               |             |           |                   |          |   |                           |
| AB5     | DSI_DATA3_N   | B22     | DSI0_TXN3    |                          |             |             |               |             |           |                   |          |   |                           |
| AB6     | GND           | -       | GND          |                          |             |             |               |             |           |                   |          |   |                           |
| AB7     | DSI_CLOCK_P   | A14     | DSI0_TXCLKP  |                          |             |             |               |             |           |                   |          |   |                           |

| OSM Pin | OSM Pin Name  | MPU Pin | GPIOx/       | Pin -Function / Mode-Mux |              |                      |              |             |            |                      |                    |   | Details |
|---------|---------------|---------|--------------|--------------------------|--------------|----------------------|--------------|-------------|------------|----------------------|--------------------|---|---------|
|         |               |         |              | 1                        | 2            | 3                    | 4            | 5           | 6          | 7                    | 8                  | 9 |         |
| AB8     | DSI_CLOCK_N   | A15     | DSI0_TXCLKN  |                          |              |                      |              |             |            |                      |                    |   |         |
| AB9     | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AB10    | DSI_DATA0_P   | B18     | DSI0_TXP0    |                          |              |                      |              |             |            |                      |                    |   |         |
| AB11    | DSI_DATA0_N   | B19     | DSI0_TXN0    |                          |              |                      |              |             |            |                      |                    |   |         |
| AB13    | USB_A_D_N     | AC4     | USB0_DM      |                          |              |                      |              |             |            |                      |                    |   |         |
| AB14    | USB_A_ID      | D20     | GPIO0_12     | OSPI0_CSn1               |              |                      |              |             |            |                      |                    |   |         |
| AB15    | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AB16    | USB_A_VBUS    | AC3     | USB0_VBUS    |                          |              |                      |              |             |            |                      |                    |   |         |
| AB17    | CAN_A_RX      | B15     | MCAN0_RX     | UART5_TXD                | TIMER_I03    | SYNC3_OUT            | UART1_RIn    | EQEP2_S     | GPIO0_97   | MCASP2_AXR1          | EHRPWM_TZn_I<br>N4 |   |         |
| AB18    | V_BAT         | N/A     |              |                          |              |                      |              |             |            |                      |                    |   |         |
| AB19    | CAN_B_RX      | N22     | MCAN1_RX     | GPMC0_WAIT1              | VOUT0_DATA23 | SPI2_CS1             | UART6_RXD    | EQEP0_I     | SPI3_D1    | GPIO0_38             | EQEP2_I            |   |         |
| AB20    | USB_B_VBUS    | AB6     | USB1_VBUS    |                          |              |                      |              |             |            |                      |                    |   |         |
| AB21    | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AB22    | USB_B_ID      | D18     | GPIO0_13     | OSPI0_CSn2               | SPI1_CS1     | OSPI0_RESET_<br>OUT1 | MCASP1_AFSR  | MCASP1_AXR2 | UART5_RXD  | ADC_EXT_TRIG<br>GER0 |                    |   |         |
| AB23    | USB_B_D_N     | AC5     | USB1_DM      |                          |              |                      |              |             |            |                      |                    |   |         |
| AC2     | PCIe_A_HS00_P | N/A     |              |                          |              |                      |              |             |            |                      |                    |   |         |
| AC3     | PCIe_A_HS00_N | N/A     |              |                          |              |                      |              |             |            |                      |                    |   |         |
| AC4     | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AC5     | DSI_DATA2_P   | A21     | DSI0_TXP2    |                          |              |                      |              |             |            |                      |                    |   |         |
| AC6     | DSI_DATA2_N   | A20     | DSI0_TXN2    |                          |              |                      |              |             |            |                      |                    |   |         |
| AC7     | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AC8     | DSI_DATA1_P   | A17     | DSI0_TXP1    |                          |              |                      |              |             |            |                      |                    |   |         |
| AC9     | DSI_DATA1_N   | A18     | DSI0_TXN1    |                          |              |                      |              |             |            |                      |                    |   |         |
| AC10    | GND           | -       | GND          |                          |              |                      |              |             |            |                      |                    |   |         |
| AC14    | USB_A_D_P     | AB4     | USB0_DP      |                          |              |                      |              |             |            |                      |                    |   |         |
| AC15    | USB_A_OC#     | Y23     | WKUP_GPIO0_6 | WKUP_CLKOUT0             |              |                      |              |             |            |                      |                    |   |         |
| AC16    | USB_A_EN      | C6      | USB0_DRVVBUS | GPIO0_124                |              |                      |              |             |            |                      |                    |   |         |
| AC17    | CAN_A_TX      | B16     | MCAN0_TX     | UART5_RXD                | TIMER_I02    | SYNC2_OUT            | UART1_DTRn   | EQEP2_I     | GPIO0_96   | MCASP2_AXR0          | EHRPWM_TZn_IN3     |   |         |
| AC18    | DEBUG_EN      | N/A     |              |                          |              |                      |              |             |            |                      |                    |   |         |
| AC19    | CAN_B_TX      | M21     | MCAN1_TX     | GPMC0_DIR                | VOUT0_DE     | SPI2_D0              | MCASP2_AXR13 | EQEP1_B     | TRC_DATA14 | GPIO0_40             | EQEP2_S            |   |         |
| AC20    | USB_B_EN      | A5      | USB1_DRVVBUS | GPIO0_125                |              |                      |              |             |            |                      |                    |   |         |
| AC21    | USB_B_OC#     | N/A     |              |                          |              |                      |              |             |            |                      |                    |   |         |
| AC22    | USB_B_D_P     | AB5     | USB1_DP      |                          |              |                      |              |             |            |                      |                    |   |         |

### 3.3. Power Supply

- > The byteENGINE can be powered with 3.3V to 5.5V.
- > The recommended power supply is 5V.



[TPS65214 Power Manage IC \(PMIC\)](#)

### 3.4. Boot modes byteENGINE AM62L

The OSM Standard implements two BOOT Pins: BOOT\_SEL0# and BOOT\_SEL1#.

The AM62L uses reduced boot mode pins (BOOT\_MODE12 ... BOOT\_MODE15). BOOT\_Mode13 and BOOT\_MODE15 are connected to vendor defined pins.

Boot Mode Pins are latched during power on reset and are automatically disconnected afterwards.

The boot sequence can be redefined with OTP writing.

| Bootmode Pin  | AM62Lx PAD      | OSM PAD          | Note                    |
|---------------|-----------------|------------------|-------------------------|
| BOOT_MODE[12] | G23 (GPMC AD12) | D14 (USART4.RXD) | OSM Boot Pin BOOT_SEL0# |
| BOOT_MODE[13] | G22 (GPMC AD13) | D13 (USART4.TXD) | Vendor Defined Pin      |
| BOOT_MODE[14] | F22(GPMC AD14)  | A14 (USART5.RXD) | OSM Boot Pin BOOT_SEL1# |
| BOOT_MODE[15] | F23 (GPMC AD15) | B13 (USART5.TXD) | Vendor Defined Pin      |

| PIN VALUE<br>BOOTMODE |    |    |    | Primary                   | Backup  |
|-----------------------|----|----|----|---------------------------|---------|
| 15                    | 14 | 13 | 12 |                           |         |
| 0                     | 1  | 1  | 0  | DEVBOOT                   | None    |
| 0                     | 1  | 1  | 1  | USB0 Host MSC             | UART    |
| 1                     | 0  | 0  | 0  | MMC0 eMMC Boot Partition  | USB DFU |
| 1                     | 0  | 1  | 0  | MMC1 4 BIT UDA Filesystem | UART    |
| 1                     | 0  | 1  | 1  | MMC0 eMMC Boot Partition  | MMC1    |
| 1                     | 1  | 1  | 0  | EXT. Host UART0           | MMC1    |
| 1                     | 1  | 1  | 1  | EXT. Host USB0 DFU        | MMC1    |



For more information, please refer to the technical reference manual of TI:  
[Technical Reference Manual](#)

## 4. Ordering info

The Order Code allows customers to easily recognize the detailed specification of the ordered SOM. Please refer to chapter [Decision guidance byteENGINE AM62L](#) for further information.

### byteENGINE-AM62L3[TYPE]-[SPEED]-[RAM]-[FLASH]-[temp-range]-[revision]

|              |                   |                                                             |
|--------------|-------------------|-------------------------------------------------------------|
| [TYPE]       | CPU type          | AM62L31 / AM62L32                                           |
| [SPEED]      | Clock speed       | 1.25 GHz                                                    |
| [RAM]        | RAM size          | 512 / 1024 / 1536 / 2048                                    |
| [FLASH]      | eMMC flash size   | 8 / 16 / 32 / 64 / 128 GB                                   |
| [temp-range] | Temperature range | [C] Consumer 0° to +85° C<br>[I] Industrial -40°C to +85 °C |
| [revision]   | Revision          | 0.1                                                         |

## 5. References byteENGINE AM62L

### NOTICE



Files can only be downloaded with login credentials. Please request your download credentials via [support@bytesatwork.ch](mailto:support@bytesatwork.ch) or contact your sales representative.



### Links

- > Schematic of OSM-S interface:  
[Schematic of OSM-S interface](#)
- > Altium Schematic of OSM-S interface:  
[Altium Schematic of OSM-S interface](#)
- > Altium Library:  
[Altium Library](#)
- > STEP Model:  
[STEP Model](#)
- > Detailed Pinout of byteENGINE AM62L:  
[Detailed Pinout of byteENGINE AM62L](#)
- > TI AM62L Series  
[AM62L Series](#)
- > bytesatwork on github:  
<https://github.com/bytesatwork>
- > byteWIKI:  
<https://bytewiki.readthedocs.io/>



# Contact Information



## Address

bytesatwork  
Technoparkstrasse 7  
CH-8406 Winterthur  
Switzerland



## Phone

Phone: +41 52 213 79 79



## Email

[support@bytesatwork.ch](mailto:support@bytesatwork.ch)



## Product Information

[www.bytesatwork.io](http://www.bytesatwork.io)



## Help

[bytewiki.readthedocs.io](http://bytewiki.readthedocs.io)

